

ABSTRACT OF THE DISCLOSURE

An array substrate for an in-plane switching liquid crystal display device includes: a gate line on a substrate; a data line crossing the gate line to define a pixel region; a thin film transistor connected to the gate line and the data line; a common line parallel to and spaced apart from the gate line; a common electrode extending from the common line and including a plurality of common electrode patterns; a pixel connecting line connected to a capacitor electrode; and a pixel electrode extending from the pixel connecting line and including a plurality of pixel electrode patterns, wherein one of the plurality of common electrode patterns and one of the plurality of pixel electrode patterns form concentric rings of pixel and common electrode patterns that are within an open region of another one of the plurality of common electrode patterns.